Designing and implementing biquad IIR filters with the ASN Filter Designer: a tutorial review

Application note (ASN-AN021) October, 2015 (Rev 2)

SYNOPSIS

Infinite impulse response (IIR) filters are useful for a variety of signal processing applications, including noise removal and speech coding. Although several practical implementations for the IIR exist, the direct form II structure is perhaps one of the most established. Thus, by virtue of the simple recursive implementation, a direct form II filtering algorithm is well suited to many fixed point micro-controller architectures. This application note specially addresses IIR biquad filter design and implementation with the ASN filter designer for both floating point and fixed point applications. The discussion is also supported by Matlab scripts allowing the reader to experiment with the concepts presented herein.

INTRODUCTION

The IIR filter implementation discussed herein is said to be ‘biquad’, since it has two poles and two zeros as illustrated below in Figure 1. The biquad implementation is particularly useful for fixed point implementations, as the effects of quantization and numerical stability are of no great concern. However, the overall success of any biquad implementation is dependent upon the available number precision, which must be sufficient enough in order to ensure that the quantised poles are always inside the unit circle.

![Figure 1 – Direct form II (biquad) IIR filter realization.](image)
Analysing Figure 1, it can be seen that the biquad structure is comprised of two feedback paths (scaled by \( a_1 \) and \( a_2 \)), two feed forward paths (scaled by \( b_1 \) and \( b_2 \)) and a section gain, \( G \). Thus, the filtering operation of Figure 1 can be summarized by the following set of recursive equations:

\[
\begin{align*}
    d(n) &= x(n) - a_1 d(n-1) - a_2 d(n-2) \\
    y(n) &= G \cdot (b_2 d(n) + b_1 d(n-1) + b_0 d(n-2))
\end{align*}
\]

(1) (2)

Analyzing Eqns. 1 and 2 and notice that the biquad implementation only requires four additions and five multiplications, which can be easily accommodated on any modern micro-controller/DSP - where, \( G \) may be included into the forward path coefficients. Also, implementing the direct form realization into two steps, i.e., Eqns. 1 and 2 significantly reduces the filter’s sensitivity to quantization error.

All biquad filter coefficients are summarised in the ASN filter designer as:

![Filter Summary](image)

Figure 2 - ASN filter designer: filter summary.

Notice that the filter summary provides the designer with a detailed overview of the designed filter, including a detailed summary of the technical specifications and the filter coefficients. The filter coefficients may be exported to third party applications for integration or further analysis, as discussed in the following section.

### 1.1. Exporting coefficients to Matlab, Scilab or ANSI C

The ASN filter designer fully supports the export of filter coefficients to Matlab, Scilab or ANSI C allowing the designer to undertake further analysis or integration with a third party algorithm.

The Default view is the standard view, which is suitable for documentation purposes. The ANSI C option is especially useful for fixed point implementation, whereby the tool produces a C header file for direct use in your application. Due to the flexibility of the data formats, developers may easily edit the files for
use with other third party applications, such as LabView, Excel etc. Import scripts for both Matlab and Scilab are shipped with the designer in order to expedite further analysis in those domains. Frameworks for Matlab, Scilab and ANSI C (including examples) can be found in the product’s installation directory.

<table>
<thead>
<tr>
<th>Directory name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>\Matlab</td>
<td>Matlab filter framework.</td>
</tr>
<tr>
<td>\Scilab</td>
<td>Scilab filter framework.</td>
</tr>
<tr>
<td>\ANSIC</td>
<td>ANSI C filter framework.</td>
</tr>
</tbody>
</table>

Independent of the Matlab framework, the following piece of Matlab code implements provides a complete implementation of the biquad filtering operation using the filter coefficients given in Figure 2:

```matlab
b=[1.0000, 1.5471, 1.0000]; % feedforward coeffs
a=[1.0000, -0.1167, 0.2047]; % feedback coeffs
G=0.3067; % section gain
bg=G*b; % pre-multiply feedforward coeffs with gain
N=100; % 100 test samples
x=randn(N,1); % generate random samples
d=zeros(3,1); % clear delay line
y=zeros(N,1); % allocate output array

for n=1:N
    d=[0;d(1:end-1)]; % update delay line
    d(1) = x(n) - a(2)*d(2) - a(3)*d(3); % compute feedback
    y(n) = bg(1)*d(1)+bg(2)*d(2)+bg(3)*d(3); % compute feedforward
end
```

Figure 3 - Matlab code for a single section biquad filter.

The ASN filter supports the design and implementation of both single section and biquad IIR filters. For any readers wishing to experiment with single section IIR filters (a subject area not covered in this application note), unchecking the Biquads checkbox in the IIR filter designer will automatically apply the single section settings. Likewise, checking the Biquads checkbox will apply the biquad settings.
1.2. Fixed point implementation

In order to implement Eqns. 1 and 2 on a fixed point processor, several important data scaling considerations must be taken into account. The following discussion relates to an implementation on a 16-bit word architecture, where the theory is equally valid for any other type of word length.

1.2.1. Quantisation

In order to correctly represent the coefficients and input/output numbers, the system word length (16-bit for the purposes of this application note) is first split up into its number of integers and fractional components. The general format is given by:

\[ Q^{\text{Num of Integers}}.^{\text{Fraction}} \]

If we assume that all of data values lie within a maximum/minimum range of ±2, we can use Q1.14 format to represent all of the numbers respectively. Notice that Q1.14 format represents a maximum of \(2 - 2^{-14} = 1.9999 = 0x7FFF\) and a minimum of \(-2 = 0x8000\) (two's complement format).

```matlab
WordLength = 16;
NumFractionalBits = 14;
Q=2^NumFractionalBits;
IntegerLength=2^(WordLength-NumFractionalBits-1);
MaxValue=IntegerLength-(1/Q);
MinValue=-IntegerLength;
```

Figure 4 - Quantisation Matlab code.

1.2.1.1. Scaling

In order to ensure that the feedback path, \(d(n)\) will not overflow, the following scaling methods are available:

- **L1 norm**: 
  \[ G = \sum_{n=0}^{\infty} |d(n)| \]
  L1 norm assumes that the input is bounded and ensures that regardless of the type of input there will be no overflow. Needless to say, L1 scaling is extreme and should only be used when L2 or LInf scaling is insufficient.

- **L2 norm**: 
  \[ G = \left[ \sum_{n=0}^{\infty} d^2(n) \right]^{1/2} \]
  L2 norm places an energy constraint on the input and output transfer function. This scaling scheme offers the smallest scaling factor.

- **LInf norm**: 
  \[ G = \max|d(w)| \]
  LInf norm ensure that the filter will not overflow when a sine wave is applied.

If \(|a_i|\leq2\) and \(|b_i|\leq2\) then the difference equations Eqns. 3 and 4 may be used:

\[ d(n) = \frac{x(n)}{G} - a_i d(n-1) - a_2 d(n-2) \]
\[ y(n) = G \times [b_v d(n) + b_1 d(n-1) + b_2 d(n-2)] \]
1.2.2. Biquad cascade scaling

A biquad cascade comprised of three biquads is shown below.

![Biquad Cascade Diagram](image)

The scaling coefficients are given as $s_1, s_2$ and $s_3$ respectively. The filter designer tool automatically scales Bq#1’s numerator coefficients by $G_1 \times \frac{s_1}{s_2}$, Bq#2’s numerator coefficients by $G_2 \times \frac{s_2}{s_3}$ and Bq#3’s numerator coefficients by $G_3$. The input scaling factor, $\frac{1}{s_1}$, and output scaling factor, $s_3$ are summarised in the filter summary under Cascade Scaling Factors. Where, Input actually given as $s_1$ instead of $\frac{1}{s_1}$ and Output is $s_3$. As a final point, rather than using the exact scaling factors, the values are actually rounded to the power of 2 (i.e. 2, 4, 8, 16 etc.) in order to simply the implementation.

### 1.2.2.1. Example

In order to fully understand the information presented in the ASN filter designer, the following example illustrates the filter coefficients obtained with double precision and with Q1.14 quantisation.

#### Biquad #1

<table>
<thead>
<tr>
<th>Gain</th>
<th>0.022065</th>
</tr>
</thead>
<tbody>
<tr>
<td>B</td>
<td>[1.000000000000, 1.6251358311, 1.000000000000]</td>
</tr>
<tr>
<td>A</td>
<td>[1.000000000000, -1.9439557907, 0.5662263801]</td>
</tr>
</tbody>
</table>

#### Biquad #2

<table>
<thead>
<tr>
<th>Gain</th>
<th>0.059999</th>
</tr>
</thead>
<tbody>
<tr>
<td>B</td>
<td>[1.000000000000, -0.2108091432, 1.000000000000]</td>
</tr>
<tr>
<td>A</td>
<td>[1.000000000000, -1.5683104516, 0.6775583399]</td>
</tr>
</tbody>
</table>

#### Biquad #3

<table>
<thead>
<tr>
<th>Gain</th>
<th>0.122786</th>
</tr>
</thead>
<tbody>
<tr>
<td>B</td>
<td>[1.000000000000, -0.7960514757, 1.0000000000]</td>
</tr>
<tr>
<td>A</td>
<td>[1.000000000000, -1.7196879441, 0.6741597332]</td>
</tr>
</tbody>
</table>

Applying L2 scaling with Q1.14 (note the effects of quantisation on the coefficient values), we obtain ($s_2 = 4$):

<table>
<thead>
<tr>
<th>Cascade Scaling Factors</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input = 8</td>
</tr>
<tr>
<td>Output = 8</td>
</tr>
</tbody>
</table>

#### Biquad #1

\[
G_1 \times \frac{s_1}{s_2} = 0.022065 \times \frac{8}{4} = 0.0441
\]

#### Biquad #2

\[
G_2 \times \frac{s_2}{s_3} = 0.059999 \times \frac{4}{8} = 0.0299
\]

#### Biquad #3

\[
G_3 = 0.1228
\]

The decimal coefficients may be directly inserted into a fixed point algorithm for implementation.

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1.2.3. Comparing spectra obtained by different arithmetic rules

In order to improve clarity and overall computation speed, the ASN filter designer only displays spectra (i.e. magnitude, phase etc.) based on the current arithmetic rules. This is somewhat different to other tools that display multi-spectra obtained by (for example) fixed point and double precision arithmetic. For any users wishing to compare spectra you may simply switch between arithmetic settings by changing the Arithmetic method (see below). The designer will then automatically re-compute the filter coefficients using the selected arithmetic rules and the current technical specification. The chart will then be updated using the current zoom settings.

1.2.4. Analytics

The ASN filter designer assists designers with extra analytics in order to help choose a suitable finite word length (FWL). The recommend FWL (RFWL) value is based on the analysis of the unquantised filter coefficients values.

For the example consider herein, it can be seen that the tool summarises the maximum and minimum coefficient values obtained with double precision. As seen, a recommendation of 14Bits is given as the largest negative value is -1.7197 which with a word length of 16Bits would require at least a fraction length of 14Bits.

1.2.5. Validating the design with the signal analyser

A design may be validated with the signal analyser, where both time and frequency domain plots are supported. A comprehensive signal generator is fully integrated into the signal analyser allowing designers to test their filters with a variety of input signals, such as sine waves, white noise or even external test data. Please refer to the ASN filter designer user’s guide (ASN15-DOC001) for more information.

For fixed point implementations the tool allows designers to specify the overflow arithmetic rules as: Saturate or Wrap. Also, the Accumulator Word Length may be set between 16-40Bits allowing designers to quickly find the optimum settings to suit their application.

1.3. Technical references

This application note assumes that the reader has a firm grasp of signal processing techniques. For any readers looking for background material, please consult the following references:

- Digital signal processing: principles, algorithms and applications, J.Proakis and D.Manoloakis
- Digital signal processing and signal processing, L.Jackson.
<table>
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<th>Rev.</th>
<th>Description</th>
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<td>10/01/2012</td>
</tr>
<tr>
<td>2</td>
<td>Document updated for v2.0</td>
<td>28/09/2015</td>
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